

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	75	capacitor and ferroelectric and (pt or ir) and pgo	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 13:32
L2	1	L1 and oxide and polycrystal\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 13:36
L3	83	pgo and ferroelectric and (pt or ir) and oxide	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:45
L4	50	pgo and ferroelectric and (pt or ir) and oxide.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:45
L5	75	pgo and ferroelectric and oxide.clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:45
S25	75	capacitor and ferroelectric and (pt or ir) and pgo	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:30
S26	14	capacitor and ferroelectric and (pt or ir) and (c adj axis adj pgo)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 13:32

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- ☒ Classification codes A: Physics, 8
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IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

☐ 1. **MFMOx ferroelectric memory transistor**
Hsu, S.T.; Li, T.K.;
[Non-Volatile Memory Technology Symposium, 2004](#)
15-17 Nov. 2004 Page(s):24 - 27
[AbstractPlus](#) | Full Text: [PDF\(1387 KB\)](#) IEEE CNF
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☐ 2. **One transistor ferroelectric memory with Pt/Pb₅Ge₃O₁₁/Ir/poly-Si/SiO₂/Si gate-stack**
Tingkai Li; Sheng Teng Hsu; Ulrich, B.D.; Stecker, L.; Evans, D.R.; Lee, J.J.;
[Electron Device Letters, IEEE](#)
Volume 23, Issue 6, June 2002 Page(s):339 - 341
Digital Object Identifier 10.1109/LED.2002.1004228
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(197 KB\)](#) IEEE JNL
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